

*Amendments to the Specification*

Please replace paragraph 25 with the following paragraph.

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A<sup>1</sup> It is preferable that the voltage be sufficiently larger than an operational supply voltage for SRAM arrays. In one embodiment, a voltage at a magnitude of equal to or greater than two times the operational supply voltage is applied. However, the actual magnitude of the voltage depends on the specific metal line layout and other process variables, as would become apparent to a person skilled in the relevant art. In another embodiment, the step of applying a voltage across adjacent bitlines or wordlines occurs at an elevated temperature.

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